

Join our Team!

Senior Software Engineer, Compiler Design, Munich

OneSpin is looking for a highly motivated software engineer to work with the R&D engineering team in the RTL Compiler-Front-End. In our agile environment we take our individual responsibility seriously and thrive on delivering expert knowledge to the team by designing, developing, troubleshooting, debugging and supporting the logic synthesis part of our software.

ONESPIN SOLUTIONS

Just think for a moment. How many digital devices have been entering your life recently? Smart watches, TVs, cars, wearables of all kinds - they all run on hardware. To stay ahead of the competition, chips must evolve quickly and be extraordinarily reliable. That's where OneSpin comes in – our software tools help the world's most innovative companies to create bug-free digital integrated circuits. OneSpin is a pioneer of advanced formal techniques to solve practical verification challenges. Focusing on formal verification of chip designs allows us to provide unique, award winning solutions for tough problems, based on a leading, high performance technology platform. Operating at the sharp end of technology, our work has a direct impact on the development processes and products of our customers across the electronics industry. Together we create the next generation of electronic products soon to hit the worldwide market.

RESPONSIBILITIES

As Compiler Design Software Engineer your work will include logic synthesis engine development, Boolean logic optimization algorithm & strategy development, data-model enhancements, and API/Class definition & enhancement. You will benefit in your teamwork to exchange ideas with most seasoned engineers to find innovative solutions in most complex challenges. You will design, program and test our (System)Verilog and SystemC frontend, transforming Abstract Syntax Trees (AST) in various steps into OneSpin's internal, language independent format.

REQUIREMENTS

The successful candidate will possess the following combination of education and experience:

MS in Computer Science or Electrical Engineering or a related subject

Proficient in C/C++

Knowledge about Verilog, VHDL, and/or SystemC is a plus

Proficient in Data structures, Algorithms, Design Patterns

Excellent programming and software engineering skills

Experience with UNIX and/or LINUX platforms

Experience with multithreaded and/or distributed programming

Strong analysis and problem solving skills

Self-motivating, able to work independently

Good communication skills

Location Munich preferred

